



Comparison of synchronous detection and $I \cdot \cos\phi$ Shunt active filtering algorithms

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ABSTRACT

The intensive use of power electronic controlled applications and non linear loads in industry by consumers cause an increasing deterioration of the power system voltage and current waveforms. As a result, harmonics are generated from power converters or nonlinear loads that cause the power system to operate with the low power factor, low efficiency, increased losses in transmission and distribution lines, failure of electrical equipments and interference problem with communication system. So, there is a great need to mitigate these harmonic and reactive current components. Active Power Filters are a viable solution to these problems. The basic principle of Active Power filter is that it generates a current equal and opposite in polarity to the harmonic current drawn by the load and injects it to the point of coupling thereby forcing the source current to be pure sinusoidal. The active power filter control has two main blocks, first one is to generate control reference compensating currents from nonlinear load current and second one is the control method to inject these compensation currents into the line. In this paper, the two control schemes for shunt active power filter namely synchronous detection algorithm and $I \cdot \cos\phi$ control algorithm are compared. Both control schemes are simulated for balanced source-balanced load, balanced source-unbalanced load and unbalanced source-balanced load conditions.

Keywords : Active Filter, Control algorithm, PWM techniques for VSC,

Introduction

The main objective of electric utilities is to supply its consumer's continuous sinusoidal voltage of constant magnitude. However this is becoming increasingly difficult, because of the rapid growth of non-linear and poor power factor loads. The use of the modern electronic equipments has changed our lives and has introduced wide variety of loads in the power systems. This has changed the load characteristics of the electric supply networks, all the electronic loads are "non-linear" and this is because of the way they draw the power from the supply. These types of loads are used for the conversion, variation and regulation of the electrical power in commercial, industrial and residential installations. The continuous usage of non-linear loads injects current and voltage harmonic components into the power system and increases reactive power demands and power system voltage fluctuations. Harmonic current components create several problems like Increase in power system losses. Oscillatory torques in rotating machinery. Significant interference with communication circuits that share common right-of-ways with AC power circuits. Reactive power burden, low system efficiency, poor power factor, system unbalance and causes excessive neutral currents. Malfunctioning of the protective relays and untimely tripping, Failure of capacitor banks Most commonly used non-linear loads are switch mode power supply system's found in personal computers. Microwave ovens, laser printers, medical to instrumentation systems, stereos, televisions and electronic lighting are also examples of

equipments using switch mode power supplies. Other types of nonlinear loads include rectifiers, both controlled and uncontrolled, and phase angle controlled power supplies. Power system contamination with harmonics deteriorates power quality and it has become a major concern for the power system engineers due to its adverse effects on sensitive loads connected to the power distribution system. There are two approaches to mitigate the power quality problems. The first approach is called load conditioning, which ensure that the equipment is made less sensitive to the power disturbances, allowing the operation of equipments even under significant voltage distortion. The other solution is to install line conditioning

systems that suppress or counteract the power system disturbances. Power electronic equipment can be designed to provide harmonic-free performance. But in most applications, the economic incentives have not been sufficient to bring about design improvements. The sinusoidal nature of the power system voltage should be preserved, while protecting components from added harmonic loadings. In order to maintain good power quality, various international agencies recommended limits of harmonic current injection into the utility. According to IEEE-519 standards the limits on the magnitudes of harmonic currents and harmonic voltage distortion at various harmonics frequencies are specified. The amount of distortion in the voltage or current waveform is quantified by means of an index called the total harmonic distortion (THD). The THD in current is defined as

$$THD = \sqrt{\sum_{h=2}^{\infty} \left(\frac{I_{sh}}{I_1}\right)^2} * 100\%$$

The magnitude of the real component of the fundamental load current in each phase is given

$$|R_e(I_{L_{a1}})| = |I_{L_a}| \cdot \cos \phi_a \tag{10}$$

$$|R_e(I_{L_{b1}})| = |I_{L_b}| \cdot \cos \phi_b \tag{11}$$

$$|R_e(I_{L_{c1}})| = |I_{L_c}| \cdot \cos \phi_c \tag{12}$$

To ensure balanced, sinusoidal currents at a unity power factor to be drawn from the source, the magnitude of the desired source current can be expressed as the average of the magnitudes of the real components of the fundamental load currents in the three phases

$$|I_{s(ref)}| = \frac{|R_e(I_{L_{a1}})| + |R_e(I_{L_{b1}})| + |R_e(I_{L_{c1}})|}{3} \tag{13}$$

$$= \frac{|I_{L_a}| \cdot \cos \phi_a + |I_{L_b}| \cdot \cos \phi_b + |I_{L_c}| \cdot \cos \phi_c}{3} \tag{14}$$

Let U_a , U_b and U_c be the unit amplitude templates of the phase-to-ground source voltages in the three phases, respectively

$$U_a = 1 \cdot \sin \omega t \tag{15}$$

$$U_b = 1 \cdot \sin(\omega t - 120^\circ) \tag{16}$$

$$U_c = 1 \cdot \sin(\omega t + 120^\circ) \tag{17}$$

The desired (reference) source currents in the three phases are therefore given as

$$i_{sa(ref)} = |I_{s(ref)}| * U_a = |I_{s(ref)}| \cdot \sin \omega t \tag{18}$$

$$i_{sb(ref)} = |I_{s(ref)}| * U_b = |I_{s(ref)}| \cdot \sin(\omega t - 120^\circ) \tag{19}$$

$$i_{sc(ref)} = |I_{s(ref)}| * U_c = |I_{s(ref)}| \cdot \sin(\omega t + 120^\circ) \tag{20}$$

The reference compensation currents for the shunt AF are thereby deduced as the difference between the actual load current and the desired source current in each phase

$$i_{sa(comp)} = i_{La} - i_{sa(ref)} \tag{21}$$

$$i_{sb(comp)} = i_{Lb} - i_{sb(ref)} \tag{22}$$

$$i_{sc(comp)} = i_{Lc} - i_{sc(ref)} \tag{23}$$

Equation (7) can be expressed as

$$i_{sa(comp)} = [Re(i_{La}) + Im(i_{La}) + harmonic\ component] - [|I_{s(ref)}| \cdot \sin \omega t] \tag{24}$$

$$i_{sb(comp)} = [Re(i_{Lb}) + Im(i_{Lb}) + harmonic\ component] - [|I_{s(ref)}| \cdot \sin(\omega t - 120^\circ)] \tag{25}$$

$$i_{sc(comp)} = [Re(i_{Lc}) + Im(i_{Lc}) + harmonic\ component] - [|I_{s(ref)}| \cdot \sin(\omega t + 120^\circ)] \tag{26}$$

PWM Techniques for Voltage Source Converters:

In many applications it is desirable to have a nearly sinusoidal output. In order to achieve sine wave output from square wave output waveform, large filters are required to filter out the low order harmonic contents from the square wave output. With PWM control it is possible to achieve sinusoidal outputs. In voltage source converters (VSC), variables to be controlled are the amplitude and frequency of the converter output voltage. PWM techniques enable the modulation of the controllable switches of the converter. By proper modulation of the controllable switches, a high frequency converter output voltage can be generated, whose low frequency or localized average during each switching cycle is same as that of the desired output voltage of the converter at that cycle. Following are the desired characteristics of the PWM techniques. Good utilization of the converter DC bus voltage. Wide linear modulation range. Low amplitudes of lower order harmonics in output voltage to minimize harmonics in output current. Low switching losses in the converter switches.

Various PWM techniques are there for getting sinusoidal output, they are

1. Sine triangle PWM
2. Selective harmonic elimination method of PWM

3. Stair case PWM
4. Hysteresis current controller.

Simulation Results:

Simulation results for Shunt APF with Balanced Source-Balanced Load condition.

Fig.4.1. Three phase source voltages

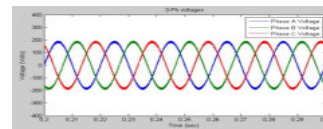


Fig.4.2. Three phase load currents

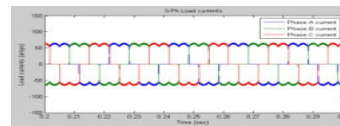


Fig.4.3. Three phase Source currents

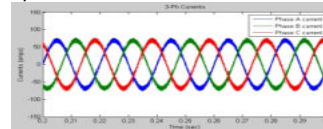
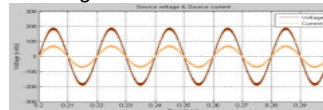


Fig.4.4. Source voltage and Source current



The load current harmonic spectrum with out the use of shunt active filter is shown in Fig 4.10. The load current Total Harmonic Distortion is 25.25%. Source current Total Harmonic Distortion is improved to 3.77% with the use of shunt active filter. Harmonic spectrum for source current after compensation is shown in Fig 4.11. Fig 4.10 Harmonic spectrum for load current

Fig.4.5. Harmonic spectrum for load current

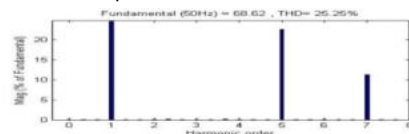
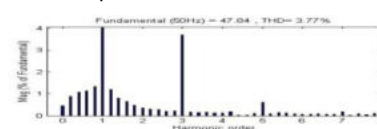


Fig.4.6. Harmonic spectrum for source current.



The THD with the use of $I \cdot \cos \phi$ algorithm for shunt active filter for balanced load balanced source has been reduced from 25.25% to 3.77%.

Simulation results for Shunt APF with Unbalanced Source-Balanced Load Condition.

Fig.4.7. Three phase Source voltages

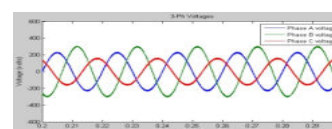


Fig.4.8. Three phase Load currents

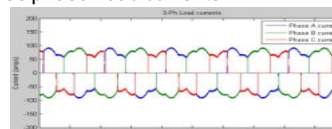


Fig.4.9.Source voltage and source current

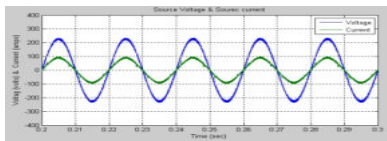


Fig.4.10.Harmonic spectrum for load current

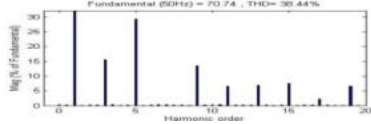
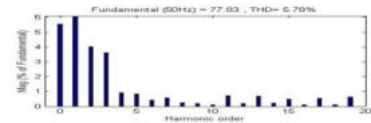


Fig.4.11.Source current Harmonic Spectrum (magnitude % of fundamental Vs Harmonic order) Total Harmonic Distortion = 5.78 %.



The THD with the use of $I_{c,cos\phi}$ algorithm for shunt active filter for unbalanced source-Balance Load has been reduced from 38.44% to 5.78%.

4.3. Simulation Results for Shunt APF with Balanced Source- Unbalanced Load Condition.

Fig.4.12.Three phase voltages

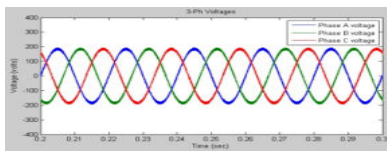


Fig 4.13 Three phase load currents

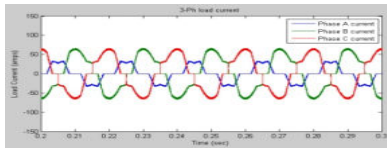


Fig.4.14. Three phase source currents

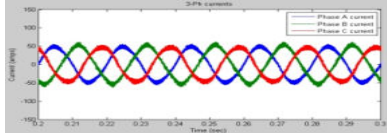


Fig.4.15. Harmonic spectrum for load current

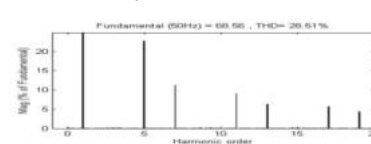
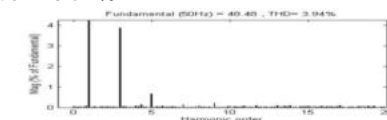


Fig. 4.16. Source current Harmonic Spectrum (magnitude % of fundamental Vs Harmonic order) Total Harmonic Distortion = 3.94 %.



Simulation Diagrams for Synchronous Detection Algorithm APF with Balanced Source-Balanced Load Condition:

Fig. 5.1 Simulink model of shunt active power filter with Balanced Source- Balanced Load.

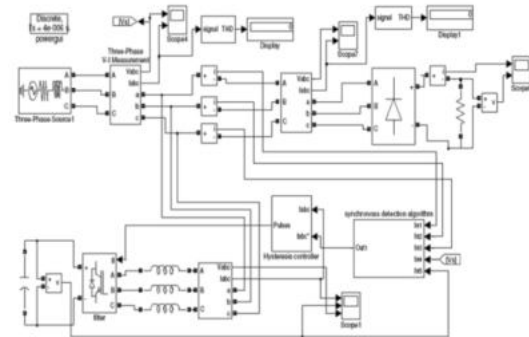


Fig.5.2 Harmonic spectrum for load current

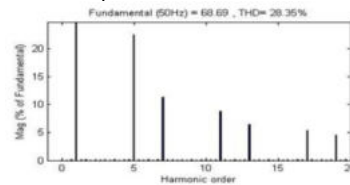
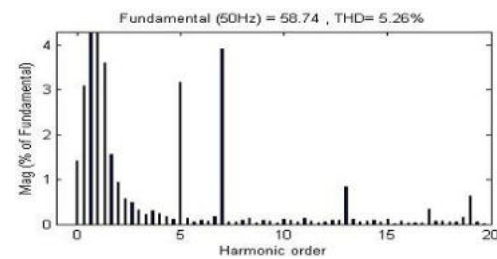


Fig.5.3 Source current Harmonic Spectrum (magnitude % of fundamental Vs Harmonic order) Total Harmonic Distortion = 5.26 %



The THD with the use of Synchronous Detection Algorithm for shunt active filter for balanced source Balanced load has been reduced from 28.35% to 5.26%.

Simulation results for APF with Balanced Source-Unbalanced Load Condition:

Fig.5.4 Harmonic spectrum for load current.

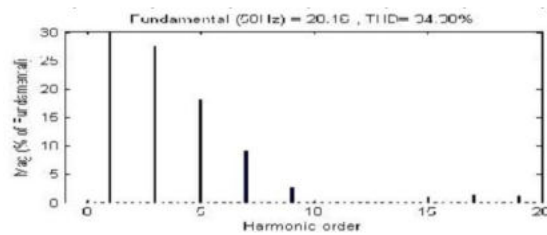
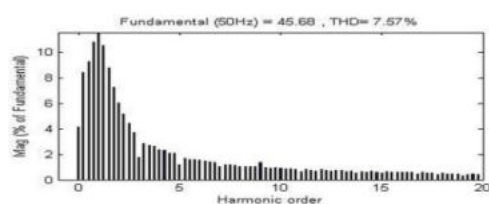


Fig. 5.5. Source current Harmonic Spectrum (magnitude % of fundamental Vs Harmonic order) Total Harmonic Distortion = 7.57 %.



The THD with the use of synchronous detection algorithm for shunt active filter for balanced source Unbalanced load has been reduced from 34.30% to 7.57%.

5.6. Simulation results for APF with Unbalanced Source-Balanced Load Condition

Fig.5.6. Harmonic spectrum for load current.

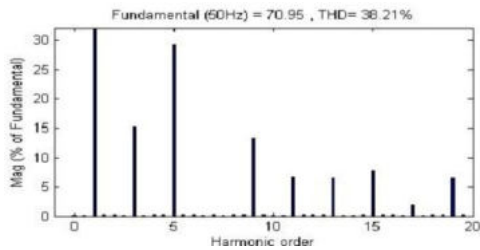
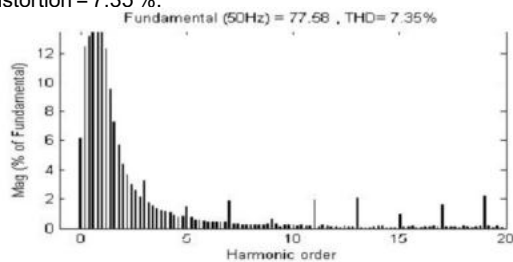


Fig 5.7. Source current Harmonic Spectrum (magnitude % of fundamental Vs Harmonic order) Total Harmonic Distortion = 7.35 %.



The THD with the use of synchronous detection algorithm for shunt active filter for unbalanced source Balanced load has been reduced from 38.21% to 7.35%.

Three phases shunt active power filter with Balanced source-Balanced load, Balanced source-Unbalanced load and Unbalanced source-Balanced load conditions are compared for both control algorithms of $I \cdot \cos\phi$ and synchronous detection algorithm. The THD is the source current for comparison under the two control schemes are shown in Table 5.1.

Comparison of the table two methods:

Table 5.1.Comparison of THD in source current

Method	THD for Balanced Source-Balanced Load	THD for Balanced Source-Unbalanced Load	THDFor Unbalanced Source-Balanced Load
Without active filter	28.35%	34.30%	38.21%
SynchronousDetection	5.26%	7.35%	7.57%

Method	THD for Balanced Source-Balanced Load	THD for Balanced Source-Unbalanced Load	THDFor Unbalanced Source-Balanced Load
Without activifilter	25.25%	28.51%	38.44%
$I \cdot \cos\phi$ Algorithm	3.77%	3.94%	5.78%

Scope for future work

Neural network or fuzzy logic based controllers can be incorporated for better response of the active filtering system.

Conclusion

Two control schemes namely $I \cdot \cos\phi$ algorithm and Synchronous Detection Algorithm used for shunt active power filter is being compared in this work. The two different schemes are analysed for non-linear load under different balanced / unbalanced source load conditions. Both of the schemes need the reactive power demand of the load. The simulation results shows that the performance of $I \cdot \cos\phi$ algorithm is reducing the Total Harmonic Distortion is better over the Synchronous Detection Algorithm.

REFERENCES

B. Singh, K. Al-Haddad, and A. Chandra, "Harmonic elimination, reactive power compensation and load balancing in three-phase, four wire electric distribution systems supplying nonlinear loads," J. Electric Power Syst. Res.vol,44, pp. 93 00, 1998. | Lucian Asiminoaei, Frede Blaabjerg, Steffan Hansen, "Evaluation of Harmonic Detection Methods for Active Power Filter Applications," IEEE Applied Power Electronics Conference and Exposition, Vol. 1, pp.635 641, 2005. | Abdelmadjid Chaoui , Jean Paul Gaubert , Fateh Krim ,G rard Champenois "PI Controlled Three-phase Shunt Active Power Filter for Power Quality Improvement"EPE proc.?electri power components and systems,volume 35,issue 12 December 2007. | Chandra.A, Singh.B, Singh B.N, Al-Haddad.K, "An improved control algorithm of shunt active filter for voltage regulation, harmonic elimination, power-factor correction, and balancing of nonlinear loads," IEEE Trans. Power Electronics, Vol. 15, pp. 495-507, May 2000. | Q. Yao and D. G. Holmes, "A simple novel method for variable hysteresis band Current control of a three phase inverter with constant switching frequency," in Conf. Rec. 28th IEEE-IAS Annu. Meeting, 1993, pp. 11221129. | Manjula G. Nair and G. Bhuvaneshwari, "A novel shunt active filter algorithm simulation and analog circuit based implementation", Special issue on Power Quality, International journal of Energy Technology and Policy (IJETP), 2006, vol 4, 1/2, pp. 118-125.69 | S. Rahmani, K. Al-Haddad & F. Fnaiech, "A three-phase shunt active power filter for damping of harmonic propagation in power distribution networks", Proc. IEEE International symposium on Industrial Electronics, vol. 3, pp.1760-1764, July 2006.